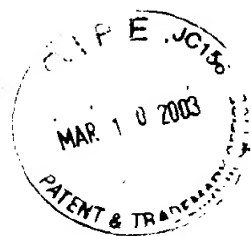


**Fig. 2**



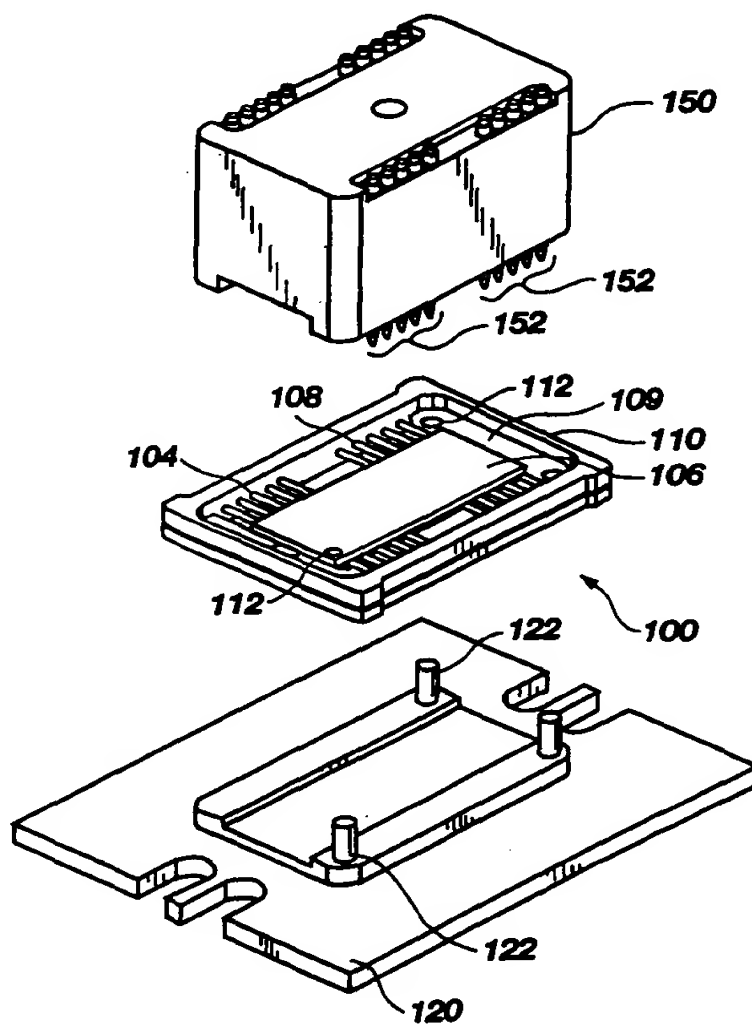
TITLE: INTEGRATED CIRCUIT PACKAGE ALIGNMENT  
FEATURE

Inventor: David J. Corisis et al.

Serial No.: 09/819,874

Docket No.: 3770.3US

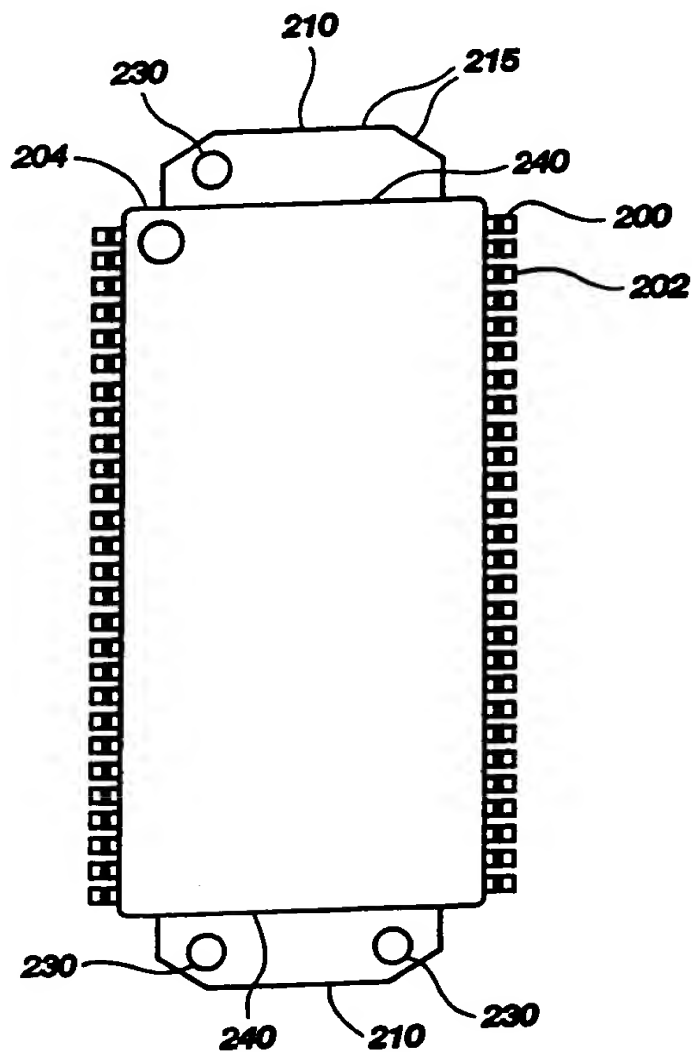
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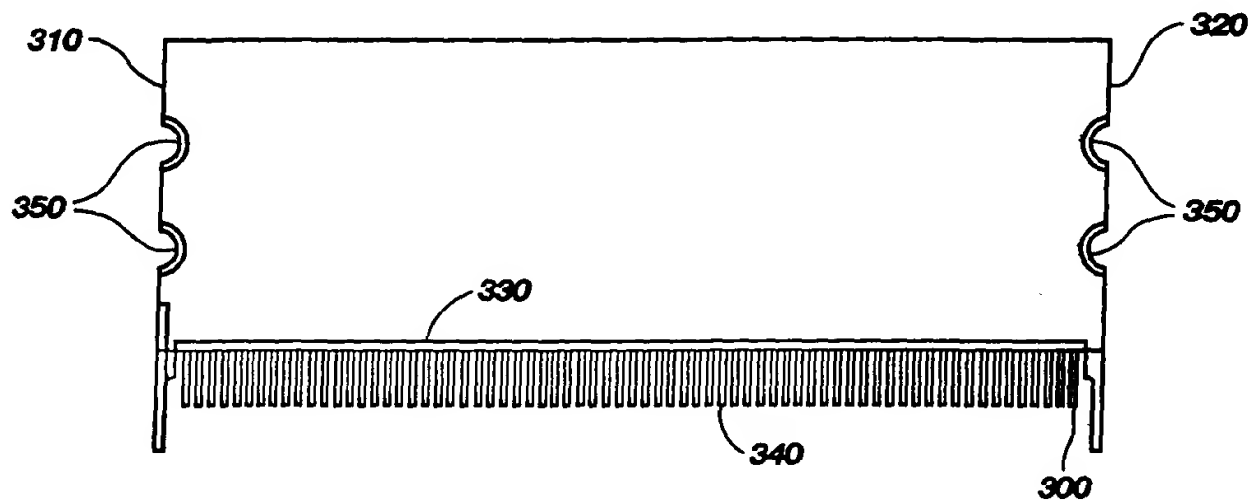
**Fig. 1**  
**(PRIOR ART)**



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**Fig. 2**



**Fig. 3**

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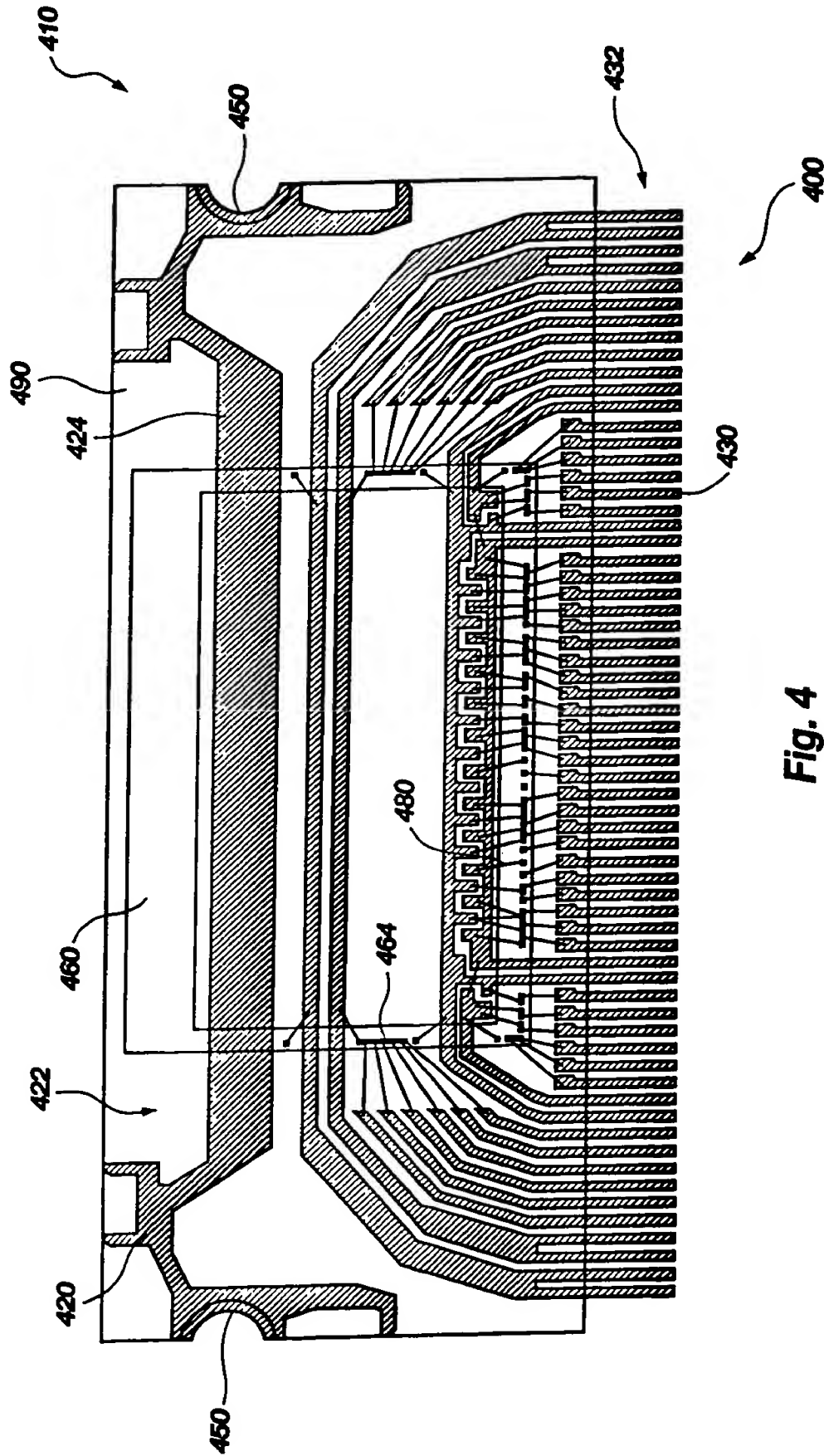


Fig. 4





TITLE: INTEGRATED CIRCUIT PACKAGE FEATURE

Inventor: David J. Corisis et al.

Serial No.: 09/819,874

Docket No.: 3770.3US

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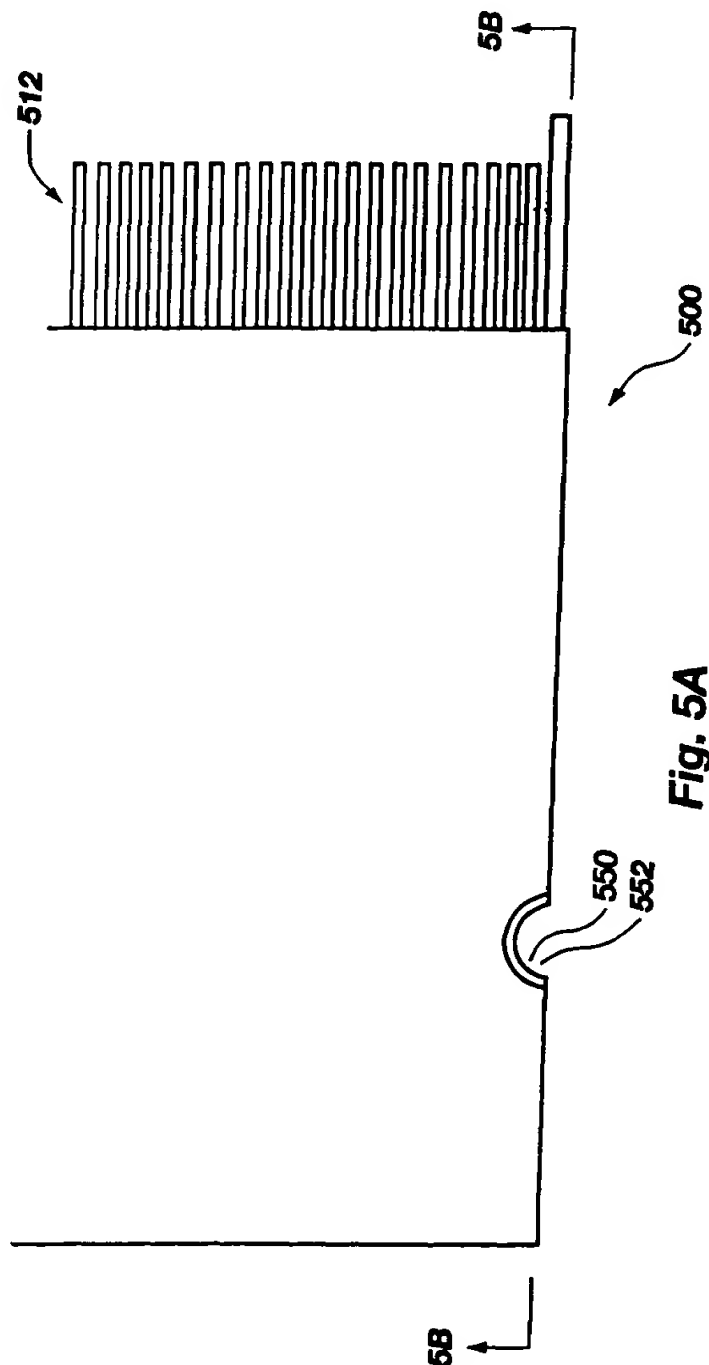


Fig. 5A

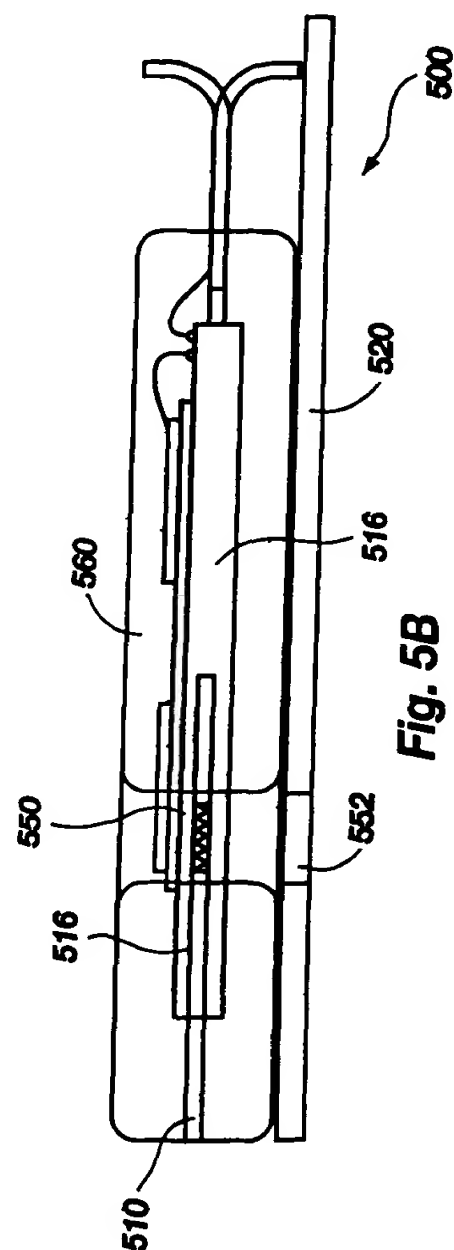


Fig. 5B

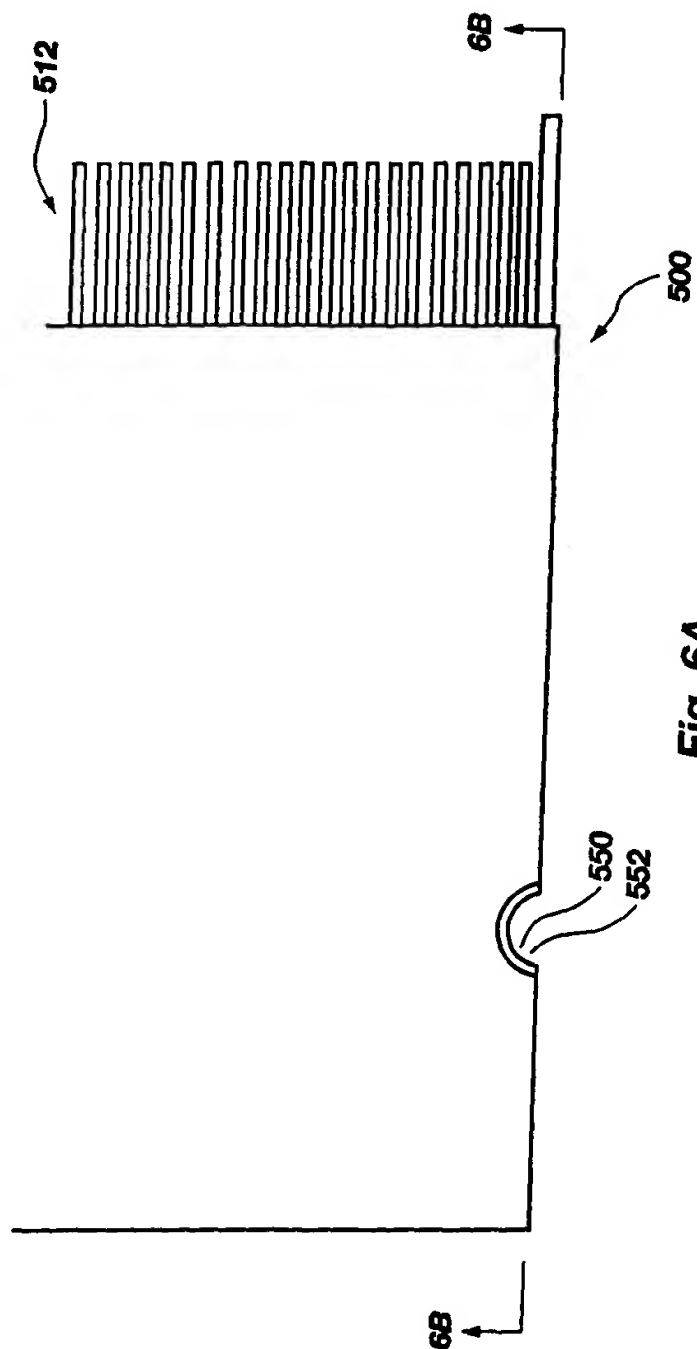


Fig. 6A

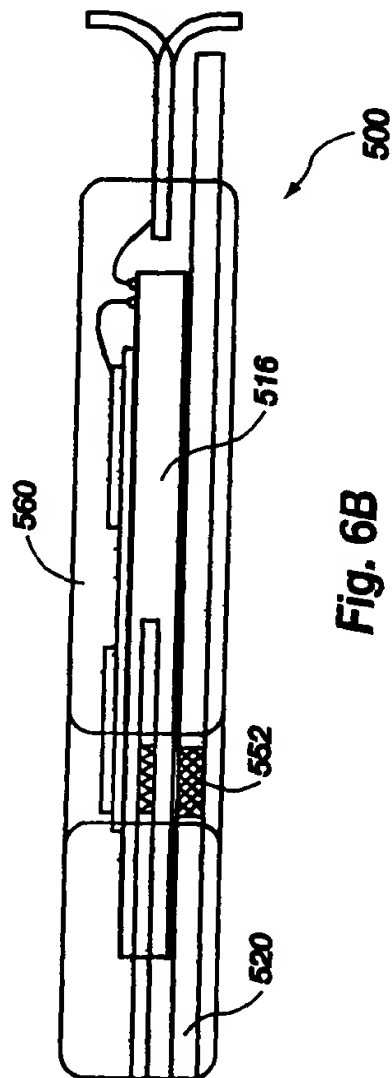


Fig. 6B

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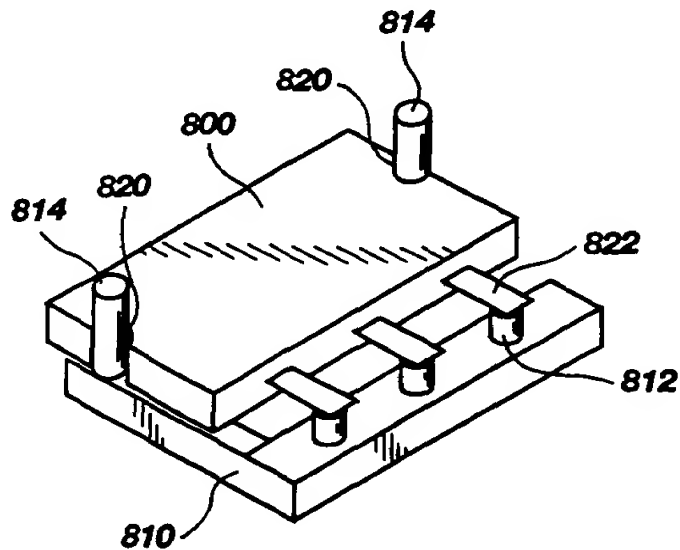
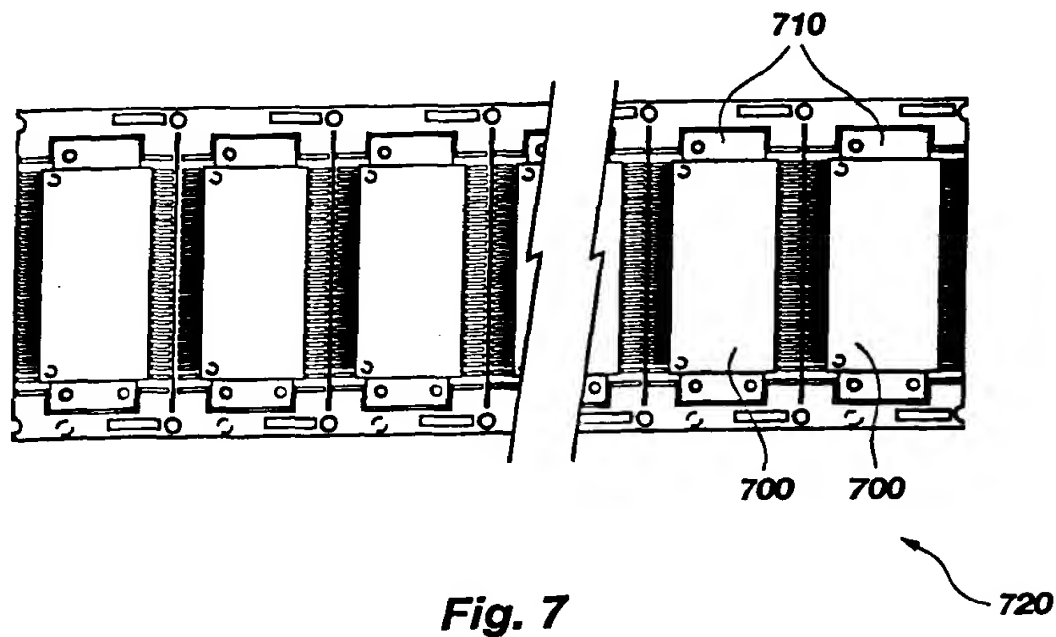


Fig. 8